

Sample of Level 2 Editing

Theoretical Study of Electron Mobility for ~~Silicon~~Silicon-Carbon Alloys

Abstract

Electron mobilities in strained Si_{1-x}C_x layers grown on a Si substrate and relaxed alloys are calculated as functions of ~~carbon~~Carbon content, alloy scattering potential, and doping concentrations at room temperature. The electron mobility model is ~~backed~~justified by experimental data. In the case of doped strained Si_{1-x}C_x, the results of our ~~model on~~electron mobility model indicates that for systems with a doping concentration ~~greater than~~above, 10¹⁸ cm⁻³, there is no substantial decrease in the in-plane mobility with an increase in the ~~Carbon mole fraction~~as the carbon mole fraction increases. However, ~~In contrast~~, for low doping concentrations, the mobility ~~decreases~~is strongly reduced with a decrease in the ~~carbon~~Carbon mole fraction ~~for low doping concentrations~~.

Comment [a1]: CHECK: Is this what was meant by this sentence. Could say “The use of the electron mobility model is justified, based on experimental data.”

I. INTRODUCTION

Charge carrier mobility in strained Si and Si_{1-x}C_x layers has attracted increasing

interest in recent years due to ~~the~~ technological progress in heteroepitaxy. Since ~~t~~he lattice constant of $\text{Si}_{1-x}\text{C}_x$ alloys ~~is~~ ~~are~~ ~~less~~ ~~smaller~~ than that of Si, ~~so~~ the strain condition of strained $\text{Si}_{1-x}\text{C}_x$ on a Si substrate is similar to that of ~~the~~ high mobility strained Si on relaxed SiGe. Thus, the $\text{Si}_{1-x}\text{C}_x$ channel embedded in Si has been proposed as an alternative to the strained Si channel. Since a graded buffer is ~~not~~ unnecessary for ~~the~~ fabrication of a strained $\text{Si}_{1-x}\text{C}_x$ layer, ~~high crystalline perfection of a~~ $\text{Si}_{1-x}\text{C}_x$ channel with high crystalline perfection is obtained. High quality pseudomorphic $\text{Si}_{1-x}\text{C}_x$ layers with up to 7% ~~carbon~~ ~~Carbon content~~ ~~up to 7%~~ have been ~~are~~ reported [1]. This alternative material ~~that~~ produces tensile strained layers, and is an attractive option because it eliminates the need to deposit a thick, relaxed SiGe buffer layer. Additionally, the elimination of this relaxed buffer layer ~~allows~~ concerns about the ~~propagation~~ ~~propagation of over a~~ high density of defect densities ($1 \times 10^{11} \text{cm}^{-2}$) ~~propagating~~ to the channel region. Theoretical calculations predict enhanced electron mobility for strained $\text{Si}_{1-x}\text{C}_x$ alloys [2], [3]. ~~The~~ Quantitative enhancement factor is strongly correlated to the assumed alloy scattering potential for the calculations [2], [3]. The Stanford group [4] has fabricated and demonstrated the operation of the surface channel of strained $\text{Si}_{1-x}\text{C}_x$ NMOSFET, characterizing ~~the~~ electron inversion mobility both at ~~room temperature and low temperature~~. However, ~~it appears that~~ the expected strain induced phonon-limited mobility enhancement has

Comment [a2]: CHECK: Did you mean to say allays here?

Comment [WL3]: IDEA: Consider an example range of these specified temperatures. i.e. Consider stating how low you mean when you mention “low temperature”.

~~been~~ appears to be compensated for by the random alloy scattering and ~~coulomb~~ Coulomb scattering associated with non-substitutional ~~carbon~~ Carbon atoms [4].

Comment [a4]: CHECK: Is this the correct interpretation of this sentence?

Recently, the UT group ~~has~~ demonstrated the buried channel strained $\text{Si}_{1-x}\text{C}_x$ PMOSFET for the first time. Strained $\text{Si}_{1-x}\text{C}_x$ PMOSFET fabricated on the $\text{Si}_{1-x}\text{C}_x$ layer ~~demonstrated~~ ~~showed~~ demonstrated enhanced hole mobility ~~compared to~~ ~~over~~ ~~that of~~ controlled Si [5]. However, the strained $\text{Si}_{1-x}\text{C}_x$ surface channel PMOSFET ~~has~~ not yet been ~~is not yet~~ fabricated and demonstrated in experiments. The LETI group was the first to report ~~present for the first time~~ epitaxially grown $\text{Si}_{1-x}\text{C}_x$ NMOSFET channels acting boron blocking barriers containing up to 1.4% substitutional ~~carbon~~ Carbon [6]. In this paper, we focus on the electron mobility in strained $\text{Si}_{1-x}\text{C}_x$ alloys. All electron mobility models for strained $\text{Si}_{1-x}\text{C}_x$ published so far ~~have~~ ~~are with~~ limited generality (e.g. ~~they~~ neglecting the influence of ~~Carbon~~ Carbon on phonon scattering [2], [3]) and ~~have not been~~ ~~are not~~ verified experimentally by the measurement of mobility in strained $\text{Si}_{1-x}\text{C}_x$ layers. This paper aims to ~~close~~ ~~at closing~~ this gap.

Comment [a5]: CHECK: The meaning of this phrase is unclear, please rewrite. Do you mean “acting as boron blocking barriers containing up to...”?

We performed a theoretical study ~~of~~ the electron mobility in strained $\text{Si}_{1-x}\text{C}_x$ alloys with a continuous variation in ~~the~~ ~~of~~ ~~carbon~~ Carbon concentration. The study ~~is that is~~ useful for future device design and simulation. In Sec. II, we discuss the theoretical models of our work ~~and~~ ~~Then in~~ Sec. III, ~~we discuss~~ ~~discusses about~~ our

results. ~~Summary is given in Sec. IV.~~Sec. IV contains the summary.

II. ~~THEORY~~

~~waswasareof~~